

STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Meta SystemsApplication No./Patent No.: 6,874,136 Filed/Issue Date: March 29, 2005Titled: CROSSBAR DEVICE WITH REDUCED PARASITIC CAPACITIVE LOADING AND USAGE OF CROSSBAR DEVICES IN RECONFIGURABLE CIRCUITS

Meta Systems, a Corporation
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest in;
 2. ☐ an assignee of less than the entire right, title, and interest in
 (The extent (by percentage) of its ownership interest is _____ %); or
 3. ☐ an assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)
- the patent application/patent identified above by virtue of either:

- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

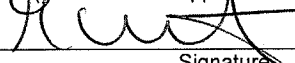
1. From: Frederic Reblewski et al. To: M2000
 The document was recorded in the United States Patent and Trademark Office at Reel 012473, Frame 0446, or for which a copy thereof is attached.
2. From: M2000 To: Abound Logic S.A.S.
 The document was recorded in the United States Patent and Trademark Office at Reel 023627, Frame 0612, or for which a copy thereof is attached.
3. From: Abound Logic To: Meta Systems
 The document was recorded in the United States Patent and Trademark Office at Reel 026030, Frame 0382, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet(s).

- ☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.



 Signature

Gianni Minutoli
 Printed or Typed Name

May 26, 2011

 Date
Attorney for Assignee

 Title

TO:GIANNI MINUTOLI COMPANY:DICKSTEIN SHAPIRO, 1825 EYE STREET, NW

**UNITED STATES PATENT AND
TRADEMARK OFFICE****Facsimile Transmission**

To: Name: GIANNI MINUTOLI
 Company: DICKSTEIN SHAPIRO, 1825 EYE STREET, NW
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37 C.F.R. 1.6 sets forth the types of correspondence that can be communicated to the Patent and Trademark Office via facsimile transmissions. Applicants are advised to use the certificate of facsimile transmission procedures when submitting a reply to a non-final or final Office action by facsimile (37 CFR 1.8(a)).

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2	<Description not available>
6	Batch 2405546 Document 1

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UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

MARCH 30, 2011

PTAS

GIANNI MINUTOLI
DICKSTEIN SHAPIRO, 1825 EYE STREET, NW
WASHINGTON, DC 20006



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PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 03/28/2011

REEL/FRAME: 026030/0382
NUMBER OF PAGES: 35

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).
DOCKET NUMBER: M1066.0000

ASSIGNOR:
ABOUND LOGIC

DOC DATE: 01/19/2011

ASSIGNEE:
META SYSTEMS
13-15 RUE JEANNE BRACONNIER -
IMMEUBLE LE PASTEUR
MEUDON LA FORET, FRANCE 92360

TO:GIANNI MINUTOLI COMPANY:DICKSTEIN SHAPIRO, 1825 EYE STREET, NW

026030/0382 PAGE 2

APPLICATION NUMBER: 11311718 FILING DATE: 12/19/2005
PATENT NUMBER: ISSUE DATE:
TITLE: CONFIGURABLE CIRCUITS WITH MICROCONTROLLERS

APPLICATION NUMBER: 11929337 FILING DATE: 10/30/2007
PATENT NUMBER: ISSUE DATE:
TITLE: MEMORY WITH FIVE-TRANSISTOR BIT CELLS AND ASSOCIATED CONTROL
CIRCUIT

APPLICATION NUMBER: 12190352 FILING DATE: 08/12/2008
PATENT NUMBER: ISSUE DATE:
TITLE: SIMULTANEOUS BI-DIRECTIONAL DATA TRANSFER

APPLICATION NUMBER: 12261912 FILING DATE: 10/30/2008
PATENT NUMBER: ISSUE DATE:
TITLE: CROSSBAR STRUCTURE WITH MECHANISM FOR GENERATING CONSTANT
OUTPUTS

APPLICATION NUMBER: 12263223 FILING DATE: 10/31/2008
PATENT NUMBER: ISSUE DATE:
TITLE: CROSSBAR DEVICE CONSTRUCTED WITH MEMS SWITCHES

APPLICATION NUMBER: 12825032 FILING DATE: 06/28/2010
PATENT NUMBER: ISSUE DATE:
TITLE: RECONFIGURABLE INTEGRATED CIRCUITS WITH SCALABLE
ARCHITECTURE INCLUDING A PLURALITY OF SPECIAL FUNCTION
ELEMENTS

APPLICATION NUMBER: 12346650 FILING DATE: 12/30/2008
PATENT NUMBER: ISSUE DATE:
TITLE: CALIBRATION OF PROGRAMMABLE I/O COMPONENTS USING A VIRTUAL
VARIABLE EXTERNAL RESISTOR

APPLICATION NUMBER: 12415507 FILING DATE: 03/31/2009
PATENT NUMBER: ISSUE DATE:
TITLE: PROGRAMMABLE DELAY LINE CIRCUIT WITH GLITCH AVOIDANCE

APPLICATION NUMBER: 12616682 FILING DATE: 11/11/2009
PATENT NUMBER: ISSUE DATE:
TITLE: RECONFIGURABLE CIRCUIT WITH REDUNDANT RECONFIGURABLE
CLUSTER(S)

APPLICATION NUMBER: 12642647 FILING DATE: 12/18/2009
PATENT NUMBER: ISSUE DATE:
TITLE: MERGED PROGRAMMABLE OUTPUT DRIVER

TO:GIANNI MINUTOLI COMPANY:DICKSTEIN SHAPIRO, 1825 EYE STREET, NW

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APPLICATION NUMBER: 12642658 FILING DATE: 12/18/2009
PATENT NUMBER: ISSUE DATE:
TITLE: DYNAMIC PHASE ALIGNMENT

APPLICATION NUMBER: 12791725 FILING DATE: 06/01/2010
PATENT NUMBER: ISSUE DATE:
TITLE: RECONFIGURABLE INTEGRATED CIRCUIT WITH A SCALABLE MESH
ARCHITECTURE

APPLICATION NUMBER: 09971349 FILING DATE: 10/04/2001
PATENT NUMBER: 6594810 ISSUE DATE: 07/15/2003
TITLE: RECONFIGURABLE INTEGRATED CIRCUIT WITH A SCALABLE
ARCHITECTURE

APPLICATION NUMBER: 10043964 FILING DATE: 01/10/2002
PATENT NUMBER: 6874136 ISSUE DATE: 03/29/2005
TITLE: CROSSBAR DEVICE WITH REDUCED PARASITIC CAPACITIVE LOADING
AND USAGE OF CROSSBAR DEVICES IN RECONFIGURABLE CIRCUITS

APPLICATION NUMBER: 11268932 FILING DATE: 11/08/2005
PATENT NUMBER: 7260218 ISSUE DATE: 08/21/2007
TITLE: CONFIGURABLE CIRCUIT WITH CONFIGURATION DATA PROTECTION
FEATURES

APPLICATION NUMBER: 11330418 FILING DATE: 01/10/2006
PATENT NUMBER: 7263456 ISSUE DATE: 08/28/2007
TITLE: ON CIRCUIT FINALIZATION OF CONFIGURATION DATA IN A
RECONFIGURABLE CIRCUIT

APPLICATION NUMBER: 11333191 FILING DATE: 01/17/2006
PATENT NUMBER: 7274215 ISSUE DATE: 09/25/2007
TITLE: RECONFIGURABLE INTEGRATED CIRCUITS WITH SCALABLE
ARCHITECTURE INCLUDING ONE OR MORE ADDERS

APPLICATION NUMBER: 11285988 FILING DATE: 11/23/2005
PATENT NUMBER: 7275196 ISSUE DATE: 09/25/2007
TITLE: RUNTIME RECONFIGURATION OF RECONFIGURABLE CIRCUITS

APPLICATION NUMBER: 11358161 FILING DATE: 02/21/2006
PATENT NUMBER: 7307873 ISSUE DATE: 12/11/2007
TITLE: MEMORY WITH FIVE-TRANSISTOR BIT CELLS AND ASSOCIATED CONTROL
CIRCUIT

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APPLICATION NUMBER: 11225750 FILING DATE: 09/13/2005
PATENT NUMBER: 7478261 ISSUE DATE: 01/13/2009
TITLE: RECONFIGURABLE CIRCUIT WITH REDUNDANT RECONFIGURABLE
CLUSTER(S)

APPLICATION NUMBER: 11840848 FILING DATE: 08/17/2007
PATENT NUMBER: 7498840 ISSUE DATE: 03/03/2009
TITLE: RECONFIGURABLE INTEGRATED CIRCUITS WITH SCALABLE
ARCHITECTURE INCLUDING ONE OR MORE ADDERS

APPLICATION NUMBER: 11840853 FILING DATE: 08/17/2007
PATENT NUMBER: 7529998 ISSUE DATE: 05/05/2009
TITLE: RUNTIME RECONFIGURATION OF RECONFIGURABLE CIRCUITS

APPLICATION NUMBER: 11358338 FILING DATE: 02/21/2006
PATENT NUMBER: 7568064 ISSUE DATE: 07/28/2009
TITLE: PACKET-ORIENTED COMMUNICATION IN RECONFIGURABLE CIRCUIT(S)

APPLICATION NUMBER: 11268933 FILING DATE: 11/08/2005
PATENT NUMBER: 7568136 ISSUE DATE: 07/28/2009
TITLE: RECONFIGURABLE SYSTEM AND METHOD WITH CORRUPTION DETECTION
AND RECOVERY

APPLICATION NUMBER: 12174565 FILING DATE: 07/16/2008
PATENT NUMBER: 7768301 ISSUE DATE: 08/03/2010
TITLE: RECONFIGURABLE INTEGRATED CIRCUITS WITH SCALABLE
ARCHITECTURE INCLUDING A PLURALITY OF SPECIAL FUNCTION
ELEMENTS

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PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ABOUND LOGIC	01/19/2011
RECEIVING PARTY DATA	
Name:	META SYSTEMS
Street Address:	13-15 Rue Jeanne Braconnier - Immeuble le Pasteur
City:	Meudon la Foret
State/Country:	FRANCE
Postal Code:	92360
PROPERTY NUMBERS Total: 25	
Property Type	Number
Application Number:	11311718
Application Number:	11929337
Application Number:	12190352
Application Number:	12261912
Application Number:	12263223
Application Number:	12825032
Application Number:	12346650
Application Number:	12415507
Application Number:	12616682
Application Number:	12642647
Application Number:	12642658
Application Number:	12791725
Patent Number:	6594810
Patent Number:	6874136
Patent Number:	7260218

OP \$1000.00 11311718

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Patent Number:	7263456
Patent Number:	7274215
Patent Number:	7275196
Patent Number:	7307873
Patent Number:	7478261
Patent Number:	7498840
Patent Number:	7529998
Patent Number:	7568064
Patent Number:	7568136
Patent Number:	7768301

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ATTORNEY DOCKET NUMBER: M1066.0000

NAME OF SUBMITTER: Gianni Minutoli

Total Attachments: 32

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